

PANIMALAR ENGINEERING COLLEGE
DEPARTMENT OF ECE
III YEAR / VI SEMESTER EVEN APRIL 2025
PRACTICAL EXAMINATION SCHEDULE
21EC1611 -VLSI DESIGN LABORATORY

FN – 8.00 AM to 11.00 AM

AN – 12.00 Noon to 3.00 PM

VENUE- 1 –VLSI LAB (LAB BLOCK –II FIRST FLOOR)

S.No.	Date / Day	Session	Batch No.	No. of students	Register No.
1.	21.04.2025 MONDAY	FN	9	32	211422106259 - 211422106290
		AN	10	32	211422106291 - 211422106322
2.	22.04.2025 TUESDAY	FN	13	32	211422106387 - 211422106418
		AN	14	32	211422106419 - 211422106450
3.	23.04.2025 WEDNESDAY	FN	1	32	211422106001 - 211422106032
		AN	2	32	211422106033 - 211422106064
4.	24.04.2025 THURSDAY	FN	5	32	211422106131 - 211422106162
		AN	6	32	211422106163 - 211422106194


LAB IN-CHARGE


HOD / ECE

Dr. S. RAJAKUMAR, M.E., Ph.D.,
Professor and Head
Electronics and Communication Engineering
Panimalar Engineering College,
Nasarathpettai, Poonamallee,
Chennai-600 123.

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21EC1611 -VLSI DESIGN LABORATORY

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VENUE-2 –IoT LAB (LAB BLOCK –VIII SECOND FLOOR)

S.No.	Date / Day	Session	Batch No.	No. of students	Register No.
1.	21.04.2025 MONDAY	FN	11	32	211422106323 - 211422106354
		AN	12	32	211422106355 - 211422106386
2.	22.04.2025 TUESDAY	FN	15	32	211422106451 - 211422106482
		AN	16	30	211422106483 - 211422106496, 211422106901 - 6916
3.	23.04.2025 WEDNESDAY	FN	3	32	211422106065 - 211422106096
		AN	4	32	211422106097 - 211422106130 (Except 211422106106 & 6107)
4.	24.04.2025 THURSDAY	FN	7	32	211422106195 - 211422106226
		AN	8	32	211422106227 - 211422106258


LAB IN-CHARGE


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PANIMALAR ENGINEERING COLLEGE
DEPARTMENT OF ECE
III YEAR / VI SEMESTER EVEN APRIL 2025
PRACTICAL EXAMINATION SCHEDULE
21EC1612 -WIRELESS COMMUNICATION AND NETWORKS LABORATORY

FN – 8.00 AM to 11.00 AM

AN – 12.00 Noon to 3.00 PM

VENUE- 1 –DSP LAB/CN LAB (LAB BLOCK-I SECOND FLOOR)

S.No.	Date / Day	Session	Batch No.	No. of students	Register No.
1.	21.04.2025 MONDAY	FN	1	32	211422106001 - 211422106032
		AN	2	32	211422106033 - 211422106064
2.	22.04.2025 TUESDAY	FN	5	32	211422106131 - 211422106162
		AN	6	32	211422106163 - 211422106194
3.	23.04.2025 WEDNESDAY	FN	9	32	211422106259 - 211422106290
		AN	10	32	211422106291 - 211422106322
4.	24.04.2025 THURSDAY	FN	13	32	211422106387 - 211422106418
		AN	14	32	211422106419 - 211422106450


LAB IN-CHARGE


HOD / ECE
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PANIMALAR ENGINEERING COLLEGE
DEPARTMENT OF ECE
III YEAR / VI SEMESTER EVEN APRIL 2025
PRACTICAL EXAMINATION SCHEDULE
21EC1612 -WIRELESS COMMUNICATION AND NETWORKS LABORATORY

FN – 8.00 AM to 11.00 AM

AN – 12.00 Noon to 3.00 PM

VENUE 2 –MICROPROCESSOR LAB (LAB BLOCK-I SECOND FLOOR)

S.No.	Date / Day	Session	Batch No.	No. of students	Register No.
1.	21.04.2025 MONDAY	FN	3	32	211422106065 - 211422106096
		AN	4	32	211422106097 - 211422106130 (Except 211422106106 & 6107)
2.	22.04.2025 TUESDAY	FN	7	32	211422106195 - 211422106226
		AN	8	32	211422106227 - 211422106258
3.	23.04.2025 WEDNESDAY	FN	11	32	211422106323 - 211422106354
		AN	12	32	211422106355 - 211422106386
4.	24.04.2025 THURSDAY	FN	15	32	211422106451 - 211422106482
		AN	16	30	211422106483 - 211422106496, 211422106901 - 6916



LAB IN-CHARGE



HOD / ECE

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**PANIMALAR ENGINEERING COLLEGE
DEPARTMENT OF ECE
PRACTICAL EXAMINATION SCHEDULE EVEN APRIL 2025
IV YEAR / VII SEMESTER (ARREAR)**

21EC1711 –EMBEDDED SYSTEMS AND ROBOTICS LABORATORY

**VENUE –VLSI LAB (LAB BLOCK –II FIRST FLOOR)
FN – 8.00 AM to 11.00 AM**

S.No.	Date / Day	Session	No. of students	Register No.	Internal Examiner
1.	22.04.2025 TUESDAY	FN	02	211421106302 & 211421106342	Mrs.G.Srividhya (2114324)



LAB IN-CHARGE



HOD / ECE

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**PANIMALAR ENGINEERING COLLEGE
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PRACTICAL EXAMINATION SCHEDULE EVEN APRIL 2025
III YEAR / V SEMESTER (ARREAR)**

21EC1511 –MICROCONTROLLERS AND INTERFACING LABORATORY

VENUE –MICROPROCESSOR LAB (LAB BLOCK-I SECOND FLOOR)

FN – 8.00 AM to 11.00 AM

S.No.	Date / Day	Session	No. of students	Register No.
1.	21.04.2025 MONDAY	FN	01	211422106125


LAB IN-CHARGE


HOD / ECE

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**PANIMALAR ENGINEERING COLLEGE
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PRACTICAL EXAMINATION SCHEDULE EVEN APRIL 2025
III YEAR / V SEMESTER (ARREAR)**

21EC1512 –DIGITAL SIGNAL PROCESSING LABORATORY

VENUE – IoT LAB (LAB BLOCK –VIII SECOND FLOOR)

FN – 8.00 AM to 11.00 AM

S.No.	Date / Day	Session	No. of students	Register No.
1.	23.04.2025 WEDNESDAY	FN	01	211422106125


LAB IN-CHARGE


HOD / ECE

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PRACTICAL EXAMINATION SCHEDULE EVEN APRIL 2025
III YEAR / V SEMESTER (ARREAR)

21EC1513 –ANALOG AND DIGITAL COMMUNICATION LABORATORY

VENUE – ADC LAB (LAB BLOCK –II FIRST FLOOR)

AN – 12.00 Noon to 3.00 PM

S.No.	Date / Day	Session	No. of students	Register No.
1.	24.04.2025 THURSDAY	AN	01	211422106125


LAB IN-CHARGE (Prof / ECE)


HOD / ECE

Dr. S. RAJAKUMAR, M.E., Ph.D.,
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**PANIMALAR ENGINEERING COLLEGE
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PRACTICAL EXAMINATION SCHEDULE EVEN APRIL 2025
II YEAR / IV SEMESTER (ARREAR)**

21EC1412 –ANALOG INTEGRATED CIRCUITS LABORATORY

VENUE – ADC LAB (LAB BLOCK –II FIRST FLOOR)

FN – 8.00 AM to 11.00 AM

S.No.	Date / Day	Session	No. of students	Register No.
1.	24.04.2025 THURSDAY	FN	01	211422106125


LAB IN-CHARGE (Prob/ECE)


HOD / ECE

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